Terms used
nonvolatile or **flash** or **eeprom** or **eprom** or **non** **volatile** **near/5** **decod** **same** **top** or **bottom** **paragraph** **poll** or

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1 eNVy: a non-volatile, main memory storage system

Michael Wu, Willy Zwaenepoel
November 1994 **ACM SIGPLAN Notices , ACM SIGOPS Operating Systems Review , Proceedi conference on Architectural support for programming languages and oper**
Issue 11 , 5
Publisher: ACM Press
Full text available: pdf(1.32 MB) Additional Information: full citation, abstract, references, citi

This paper describes the architecture of eNVy, a large non-volatile main memory storage syster presents its storage space as a linear, memory mapped array rather than as an emulated disk ir use software interface.Flash memories provide persistent storage with solid-state memory acces state technologies. However, they have a number of drawbacks. Flash chips are ...

2 Serverless network file systems

Thomas E. Anderson, Michael D. Dahlin, Jeanna M. Neefe, David A. Patterson, Drew S. Roselli, Rar
February 1996 **ACM Transactions on Computer Systems (TOCS)**, Volume 14 Issue 1
Publisher: ACM Press
Full text available: pdf(2.69 MB) Additional Information: full citation, abstract, references, citi

We propose a new paradigm for network file system design: serverless network file systems. Wl a central server machine, a serverless system utilizes workstations cooperating as peers to prov in the system can store, cache, or control any block of data. Our approach uses this location ind area networks, to provide better performance and scalability th ...

Keywords: RAID, log cleaning, log structured, log-based striping, logging, redundant data stor

3 Session summaries from the 17th symposium on operating systems principle (SOSP'99)

Jay Lepreau, Eric Eide
April 2000 **ACM SIGOPS Operating Systems Review**, Volume 34 Issue 2
Publisher: ACM Press
Full text available: pdf(3.15 MB) Additional Information: full citation, index terms

4 Efficient management for large-scale flash-memory storage systems with resource conser

Li-Pin Chang, Tei-Wei Kuo
November 2005 **ACM Transactions on Storage (TOS)**, Volume 1 Issue 4

Publisher: ACM Press

Full text available: pdf(1.45 MB) Additional Information: full citation, abstract, references, ind

Many existing approaches on flash-memory management are based on RAM-resident tables in w both address translation and space management. As high-capacity flash memory is becoming m how to manage the RAM space or how to improve the access performance is emerging for many tree-based management scheme which adopts multiple granularities in flash-memory managem

Keywords: Flash memory, consumer electronics, embedded systems, memory management, p

5 Design and evaluation of dynamic optimizations for a Java just-in-time compiler

Toshio Suganuma, Toshiaki Yasue, Motohiro Kawahito, Hideaki Komatsu, Toshio Nakatani

July 2005 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volu

Publisher: ACM Press

Full text available: pdf(1.60 MB) Additional Information: full citation, abstract, references, citi

The high performance implementation of Java Virtual Machines (JVM) and Just-In-Time (JIT) cor dynamic compilation system on the basis of online runtime profile information. The trade-off bet performance benefit is a crucial issue for such a system. This article describes the design and im framework in a production-level Java JIT compiler, together with two techniques for profile-direc

Keywords: JIT compiler, Recompilation, adaptive optimization, code specialization, dynamic co

6 Cache coherence tradeoffs in shared-memory MPSoCs

Mirko Loghi, Massimo Poncino, Luca Benini

May 2006 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 5 Issue 2

Publisher: ACM Press

Full text available: pdf(707.54 KB) Additional Information: full citation, abstract, references, ind

Shared memory is a common interprocessor communication paradigm for single-chip multiproce coherence is a very successful technique that provides a clean shared-memory programming ab multiprocessors, but there is no consensus on its usage in resource-constrained multiprocessor s applications. This work aims at providing a comparative energy and performance analysis of cac

Keywords: Cache coherence, low power, multiprocessor, system-on-chip

7 Risks to the public in computers and related systems

Peter G. Neumann

January 1990 **ACM SIGSOFT Software Engineering Notes**, Volume 15 Issue 1

Publisher: ACM Press

Full text available: pdf(2.11 MB) Additional Information: full citation

8 Sensor networks and performance analysis: Java™ on the bare metal of wireless sensor c

machine

Doug Simon, Cristina Cifuentes, Dave Cleal, John Daniels, Derek White

June 2006 **Proceedings of the 2nd international conference on Virtual execution envir**

Publisher: ACM Press

Full text available: pdf(999.55 KB) Additional Information: full citation, abstract, references, ind

The Squawk virtual machine is a small Java™ virtual machine (VM) written mostly in Java that r

wireless sensor platform. Squawk translates standard class file into an internal pre-linked, positi and allows for efficient execution of bytecodes that have been placed into a read-only memory. application isolation mechanism whereby applications are represented as object and are therefo

Keywords: IEEE 802.15.4, Java virtual machine, Sun SPOT, embedded systems, wireless sensc

9 Systems, platforms, and applications: MANTIS: system support for multimodAl NeTworks

H. Abrach, S. Bhatti, J. Carlson, H. Dai, J. Rose, A. Sheth, B. Shucker, J. Deng, R. Han
September 2003 **Proceedings of the 2nd ACM international conference on Wireless sensor r**
Publisher: ACM Press
Full text available: pdf(424.53 KB) Additional Information: full citation, abstract, references, citi

The *MANTIS M*ultimod*A*l system for *NeT*works of In-situ wireless Sensors provides a new multitr integrated with a general-purpose single-board hardware platform to enable flexible and rapid p The key design goals of MANTIS are ease of use, i.e. a small learning curve that encourages nov novel sensor networking applications in software and hardware, as well as flexibility, ...

Keywords: GPS, dynamic reprogramming, lightweight, multimodal prototyping, operating syste

10 The Howitzer improvement program: lessons learned

D. Krantz
January 1989 **Proceedings of the conference on Tri-Ada '89: Ada technology in context: a deployment TRI-Ada '89**
Publisher: ACM Press
Full text available: pdf(1.59 MB) Additional Information: full citation, references, index terms

11 System architecture directions for networked sensors

Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister
November 2000 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Archite international conference on Architectural support for programming langua IX**, Volume 34 , 28 Issue 5 , 5
Publisher: ACM Press
Full text available: pdf(299.01 KB) Additional Information: full citation, abstract, references, citi

Technological progress in integrated, low-power, CMOS communication devices and sensors mal sensors viable. They can be deeply embedded in the physical world and spread throughout our e elements are an overall system architecture and a methodology for systematic advance. To this develop a small device that is representative of the class, design a tiny event-driven operating s

12 System architecture directions for networked sensors

Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister
November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11
Publisher: ACM Press
Full text available: pdf(1.32 MB) Additional Information: full citation, abstract, references, citi

Technological progress in integrated, low-power, CMOS communication devices and sensors mal sensors viable. They can be deeply embedded in the physical world and spread throughout our e elements are an overall system architecture and a methodology for systematic advance. To this develop a small device that is representative of the class, design a tiny event-driven operating s

13 Is it live or is it Memorex?

Tory Sawyer, Randy Anderson, Gary McCuaig

September 1986 **Proceedings of the 14th annual ACM SIGUCCS conference on User services**
Publisher: ACM Press
Full text available: pdf(2.60 MB) Additional Information: full citation, index terms

14 Report of the national workshop on internet voting: issues and research agenda
C. D. Mote
May 2002 **Proceedings of the 2002 annual national conference on Digital government r**
Publisher: Digital Government Research Center
Full text available: pdf(539.99 KB) Additional Information: full citation

15 Report of the national workshop on internet voting: issues and research agenda
C. D. Mote
May 2000 **Proceedings of the 2000 annual national conference on Digital governmen**
Publisher: Digital Government Research Center
Full text available: pdf(539.99 KB) Additional Information: full citation, abstract

As use of the Internet in commerce, education and personal communication has become commc and national elections naturally arises. In addition to adding convenience and precision, some b historical and downward trend of voter turnout in the United States. For these reasons President December 1999 requesting that the National Science Foundation examine the feasibility of onlin

16 Systems II: Hardware design experiences in ZebraNet
Pei Zhang, Christopher M. Sadler, Stephen A. Lyon, Margaret Martonosi
November 2004 **Proceedings of the 2nd international conference on Embedded networked**
Publisher: ACM Press
Full text available: pdf(472.66 KB) Additional Information: full citation, abstract, references, citi

The enormous potential for wireless sensor networks to make a positive impact on our society h the topic, and this research is now producing environment-ready systems. Current technology li application requirements lead to a diversity of hardware platforms for different portions of the d energy and reliability constraints of a system that must function for months at a time without h

Keywords: GPS, ZebraNet, sensor deployment, sensor networks

17 Distributed operating systems
Andrew S. Tanenbaum, Robbert Van Renesse
December 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 4
Publisher: ACM Press
Full text available: pdf(5.49 MB) Additional Information: full citation, abstract, references, citi

Distributed operating systems have many aspects in common with centralized ones, but they al intended as an introduction to distributed operating systems, and especially to current universit of what constitutes a distributed operating system and how it is distinguished from a computer discussed. Then several examples of current research projects are examined in some detail ...

18 Shared memory computing on clusters with symmetric multiprocessors and system area n
Leonidas Kontothanassis, Robert Stets, Galen Hunt, Umit Rencuzogullari, Gautam Altekar, Sandhy
August 2005 **ACM Transactions on Computer Systems (TOCS)**, Volume 23 Issue 3
Publisher: ACM Press
Full text available: pdf(918.28 KB) Additional Information: full citation, abstract, references, ind

Cashmere is a software distributed shared memory (S-DSM) system designed for clusters of ser from most other S-DSM projects by (1) the effective use of fast user-level messaging, as provid (2) a "two-level" protocol structure that exploits hardware coherence within multiprocessor node tradeoffs in coherence protocol design; they allow Cashmere to employ a relatively simp ...

Keywords: Distributed shared memory, relaxed consistency, software coherence

19 Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on**

Publisher: IBM Press

Full text available: pdf(4.21 MB) Additional Information: full citation, abstract, references, ind

Understanding distributed applications is a tedious and difficult task. Visualizations based on pro obtain a better understanding of the execution of the application. The visualization tool we use i University of Waterloo. However, these diagrams are often very complex and do not provide the application. In our experience, such tools display repeated occurrences of non-trivial commun ..

20 Work-in-progress session on innovative topics: Security wrappers and power analysis for S

C. H. Gebotys, Y. Zhang

October 2003 **Proceedings of the 1st IEEE/ACM/IFIP international conference on Hardw synthesis CODES+ISSS '03**

Publisher: ACM Press

Full text available: pdf(790.57 KB) Additional Information: full citation, abstract, references, citi

Future wireless internet enabled devices will be increasingly powerful supporting many more ap security. Although SoCs offer more resistance to bus probing attacks, power/EM attacks on core malicious code are relevant. This paper presents a methodology for security on NoC at both the the core level (or application layer) is proposed. For the first time a low cost security wrapp ...

Keywords: VLIW, adiabatic, design, performance, security

Results 1 - 20 of 200 Result page: **1** 2 3 4 5 6 7 8 9 10

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	13	(Paolino near2 Schillaci).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L2	12	(Salvatore near2 Poli).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L3	11	(Antonino near2 Malfa).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L4	13	(Paolino near2 Schillaci).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L5	12	(Salvatore near2 Poli).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L6	11	(Antonino near2 Malfa).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L7	6	L4 and L5 and L6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L8	53422	nonvolatile adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L9	121930	flash adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01

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L10	474119	eeprom or eprom or rom	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L11	36783	select$4 adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L12	127431	select$4 near3 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L13	18553	decod$4 same (top or bottom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L14	127431	select$4 near3 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L15	18553	decod$4 same (top or bottom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L16	3117	L14 and L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L17	295	L14 same L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L18	1536016	pin$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L19	295	L14 same L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01

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L20	1536016	pin$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L21	58	L19 and L20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L22	5	L19 same L20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L23	22771915	@ad<"20020718"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L24	11	(Antonino near2 Malfa).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L25	12	(Salvatore near2 Poli).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L26	13	(Paolino near2 Schillaci).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:01
L27	13	(Paolino near2 Schillaci).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L28	12	(Salvatore near2 Poli).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L29	11	(Antonino near2 Malfa).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02

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L30	13	(Paolino near2 Schillaci).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L31	12	(Salvatore near2 Poli).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L32	11	(Antonino near2 Malfa).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L33	6	L30 and L31 and L32	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L34	53422	nonvolatile adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L35	474119	eeprom or eprom or rom	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L36	121930	flash adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
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L38	127431	select$4 near3 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L39	18553	decod$4 same (top or bottom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02

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L40	127431	select$4 near3 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L41	18553	decod$4 same (top or bottom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:02
L42	22771915	@ad<"20020718"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L43	127431	select$4 near3 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L44	18553	decod$4 same (top or bottom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L45	295	L43 same L44	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L46	1536016	pin$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L47	5	L45 same L46	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L48	43260	(most adj significant adj bit) or MSB	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L49	262	(memory adj address) same poll$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03

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L50	41	L48 and L49	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L51	75879	"365"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L52	3	L50 and L51	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L53	3	L42 and L52	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03
L54	0	L47 and L53	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 12:03